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PPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/665,576	09/22/2003	Jong Dam Kim	2658-0311P	6443
2292	7590 06/22/2005		EXAMINER	
BIRCH STEWART KOLASCH & BIRCH PO BOX 747			PATEL, PARESH H	
FALLS CHURCH, VA 22040-0747			ART UNIT	PAPER NUMBER
			2829	
		DATE MAILED: 06/22/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)		
	10/665,576	KIM ET AL.		
Office Action Summary	Examiner	Art Unit		
1	Paresh Patel	2829		
The MAILING DATE of this communication app	ears on the cover sheet with the c	orrespondence address		
Period for Reply  A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w  - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	86(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	ely filed s will be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).		
Status				
<ol> <li>Responsive to communication(s) filed on <u>09 Marge</u></li> <li>This action is <b>FINAL</b>. 2b) This</li> <li>Since this application is in condition for allowant closed in accordance with the practice under Exercise</li> </ol>	action is non-final. nce except for formal matters, pro			
Disposition of Claims				
4)  Claim(s) 1-22 is/are pending in the application. 4a) Of the above claim(s) 5-10 and 15-22 is/are 5)  Claim(s) is/are allowed. 6)  Claim(s) 1,2,4,11,12 and 14 is/are rejected. 7)  Claim(s) 3 and 13 is/are objected to. 8)  Claim(s) are subject to restriction and/or	withdrawn from consideration.			
Application Papers				
9) The specification is objected to by the Examiner 10) The drawing(s) filed on 22 September 2003 is/a Applicant may not request that any objection to the of Replacement drawing sheet(s) including the correction 11) The oath or declaration is objected to by the Examiner	re: a)⊠ accepted or b)⊡ object drawing(s) be held in abeyance. See ion is required if the drawing(s) is obj	ected to. See 37 CFR 1.121(d).		
Priority under 35 U.S.C. § 119				
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>				
Attachment(s)  1) Notice of References Cited (PTO-892)  2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:			

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### **DETAILED ACTION**

## Response to Arguments

1. Applicants' arguments filed 05/09/2005 with respect to Cheng references have been fully considered but they are not persuasive. Applicant argues that Cheng fails to teach the jump line 20 short-circuiting any of the ESD devices on the signal lines 10. Examiner disagrees with Applicant because at lines 17-45 of column 3 (particularly at 35-45), Cheng discloses ESD protection devices are coupled to the circuits of the LCD panels to protect TFT from ESD events. Applicants' in fig. 2 discloses ESD devices 2a-2c with shorting wire 4 and pads 6a-6c. Cheng's discloses similar structure in fig. 2 including shorting wire 20 that is a part of ESD protection formed on the substrate and connected to the signal lines 10 during testing using tester 40. Therefore, Cheng discloses short-circuiting the ESD devices using shorting wire 20 because shorting wire 20 is connected to LCD via signal lines 10 and ESD protection devices is also connected to LCD.

## Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

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3. Claims 1-2, 4, 11-12 and 14 are rejected under 35 U.S.C. 102(b) as being anticipated by Cheng et al. (US 2002/0063574 A1 May 30, 2002 now US 6720791).

Regarding claim 1 and 11, Cheng et al. (hereafter Cheng) in fig. 2-5 discloses an apparatus/method for inspecting a display device substrate having a plurality of signal wirings and a plurality of electrostatic discharge damage (ESD) protection devices, the apparatus comprising:

a conductive shorting bar [20 and lines 40-42 of column 3] to short the ESD protection devices [ESD];

a power supply [40] to supply [using probe tip at pad 3] a current to the signal wirings [10 or Pi-Pn and electrodes of pixel]; and

a detection circuit [40] to determine a defectiveness of at least one of the signal wirings depending on the current flowing [S1] on the signal wirings.

Regarding claims 2 and 12, Chang discloses the conductive shorting bar is provided in a jig [lines 28-30, column 3].

4. Regarding claims 4 and 14, Chang discloses the display device substrate is a TFT array substrate of a liquid crystal display [lines 35-37 of column 3].

#### Conclusion

5. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within

TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Paresh Patel whose telephone number is 571-272-1968. The examiner can normally be reached on 8:00 to 4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nestor Ramirez can be reached on 571-272-2034. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Paresh Patel Primary Examiner Art Unit 2829 Page 4

June 20, 2005